AW-CM467-SUR

IEEE 802.11 1X1 a/b/g/n/ac Wireless LAN and

Bluetooth 5

12x12 LGA module

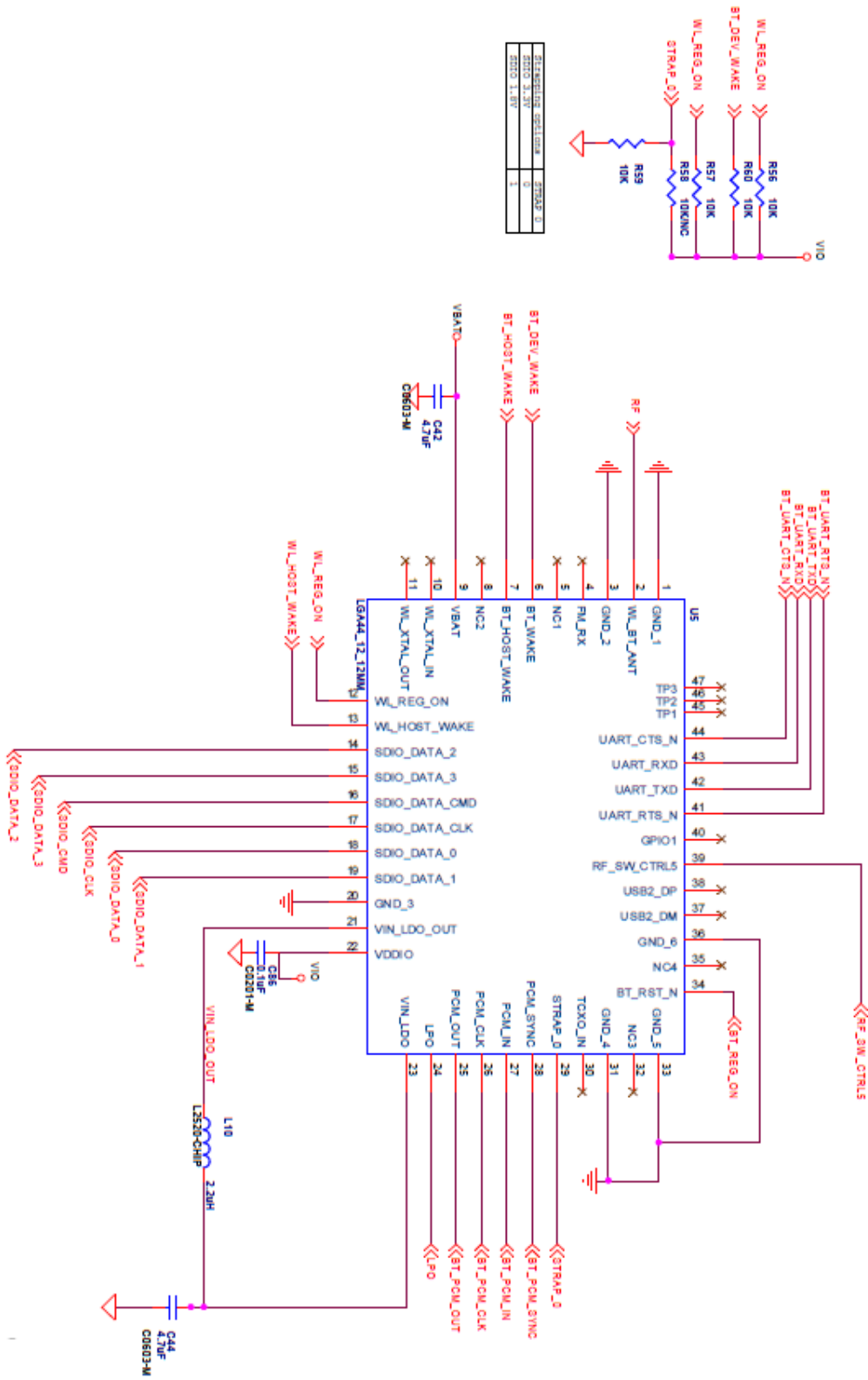# Reference Design Guide

Rev. A

(For Standard)

Revision History

Version	Revision Date	Description	Initials	Approved
A	2021/04/14	• Initial Version	JM.Pang	N.C. Chen

STRAP_0

WLREG_ON
BT_DEV_WAKE
WLREG_ON

R59 10K

R58 10K/NC
R57 10K
R60 10K
R56 10K

VIO

Bit setting options		
		STRAP_0
SDIO 3.3V		0
SDIO 1.8V		1

VBAT0

C42 4.7uF C0603-M

BT_DEV_WAKE
BT_HOST_WAKE

RF

BT_UART_RTS_N
BT_UART_TXD
BT_UART_RXD
BT_UART_CTS_N

WL_HOST_WAKE
WL_REG_ON

U5 — LGA44_12_12MM

Pin	Name		Pin	Name
11	WL_XTALOUT			
10	WL_XTALIN			
9	VBAT			
8	NC2			
7	BT_HOST_WAKE			
6	BT_WAKE			
5	NC1			
4	FM_RX			
3	GND_2			
2	WL_BT_ANT			
1	GND_1			
12	WL_REG_ON		47	TP3
13	WL_HOST_WAKE		46	TP2
14	SDIO_DATA_2		45	TP1
15	SDIO_DATA_3		44	UART_CTS_N
16	SDIO_DATA_CMD		43	UART_RXD
17	SDIO_DATA_CLK		42	UART_TXD
18	SDIO_DATA_0		41	UART_RTS_N
19	SDIO_DATA_1		40	GPIO1
20	GND_3		39	RF_SW_CTRL5
21	VIN_LDO_OUT		38	USB2_DP
22	VDDIO		37	USB2_DM
			36	GND_6
			35	NC4
			34	BT_RST_N

23	VIN_LDO
24	LPO
25	PCM_OUT
26	PCM_CLK
27	PCM_IN
28	PCM_SYNC
29	STRAP_0
30	TCXO_IN
31	GND_4
32	NC3
33	GND_5

C86 0.1uF C0201-M

VIO

L10 L2520-CHIP 2.2uH

VIN_LDO_OUT

C44 4.7uF C0603-M

SDIO_DATA_2
SDIO_DATA_3
SDIO_CMD
SDIO_CLK
SDIO_DATA_0
SDIO_DATA_1

LPO
BT_PCM_OUT
BT_PCM_IN
BT_PCM_CLK
BT_PCM_SYNC
STRAP_0

BT_REG_ON
RF_SW_CTRL5